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PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Akhil Garlapati et al.

Title: VOLTAGE REFERENCE GENERATOR CIRCUIT USING LOW-BETA  
EFFECT OF A CMOS BIPOLAR TRANSISTOR

Application No.: 10/813,837

Filed: March 31, 2004

Examiner: Rajnikant B. Patel

Group Art Unit: 2838

Atty. Docket No.: 026-0044

Confirmation No.: 6047

January 9, 2006

Mail Stop AF  
 Commissioner for Patents  
 P.O. Box 1450  
 Alexandria, VA 22313-1450

**NOTICE OF APPEAL FROM THE EXAMINER TO THE BOARD OF  
PATENT APPEALS AND INTERFERENCES**

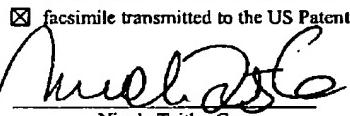
Applicant(s) hereby appeal(s) to the Board of Patent Appeals and Interferences from the last decision of the Examiner.

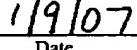
The fee for this Notice of Appeal under 37 C.F.R. § 41.20(b)(1) is submitted as set forth in the accompanying transmittal letter.

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I hereby certify that, on the date shown below, this correspondence is being

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 Nicole Teitler Cave

  
 1/9/07  
 Date

Respectfully submitted,

  
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notice of appeal

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